

**ABSTRACT OF THE DISCLOSURE**

A circuit is provided for controlling the gain of an amplifier, such as an amplifier that may be used in telecommunication devices. The circuit includes an analog portion that receives signals from one or more mixers and that produces an output that is an inverse logarithmic function of the input signal. The output signal is digitally processed by a digital portion of the circuit, which determines whether gain of the amplifier should be increased, decreased, or held constant. Also, methods are provided for controlling the gain of an amplifier.